

AS5040/AS5045/AS5140/AS5145/AS5145-I10(-I12)

Programmable Magnetic Rotary Encoder

AS5040-AB-v2.0

Adapterboard

OPERATION MANUAL

1 General Description

The AS5x40/AS5x45 series are contactless magnetic rotary encoders for accurate angular measurement over a full turn of 360°. They are system-on-chip, combining integrated Hall elements, analog front end and digital signal processing in a single device.

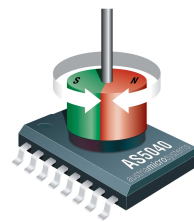
To measure the angle, only a simple two-pole magnet, rotating over the center of the chip, is required. The magnet may be placed above or below the IC.

The absolute angle measurement provides instant indication of the magnet's angular position with a resolution of $0.35^\circ = 1024$ (AS5x40 – 10bit) or $0.0879^\circ = 4096$ (AS5x45 – 12bit) positions per revolution. This digital data is available as a serial bit stream and as a PWM signal.

Furthermore, a user-programmable incremental output is available, making the chip suitable for replacement of various optical encoders.

The AS5145 is also available as 10bit or 12bit pre-programmed version AS5145-I10(-I12).

An internal voltage regulator allows the AS5x40/AS5x45 to operate at either 3.3 V or 5 V supplies.



2 The AS5040 Adapter board

2.1 Board description

The AS5040 adapter board is a simple circuit allowing test and evaluation of the AS5040/AS5045/AS5140/AS5145/AS5145-I10/AS5145-I12 rotary encoder quickly without building a test fixture or PCB.

The PCB can be used as standalone unit or attached to a microcontroller. The standalone operation requires a 5V power supply only; the magnet's angle can be read on the PWM or analog output.

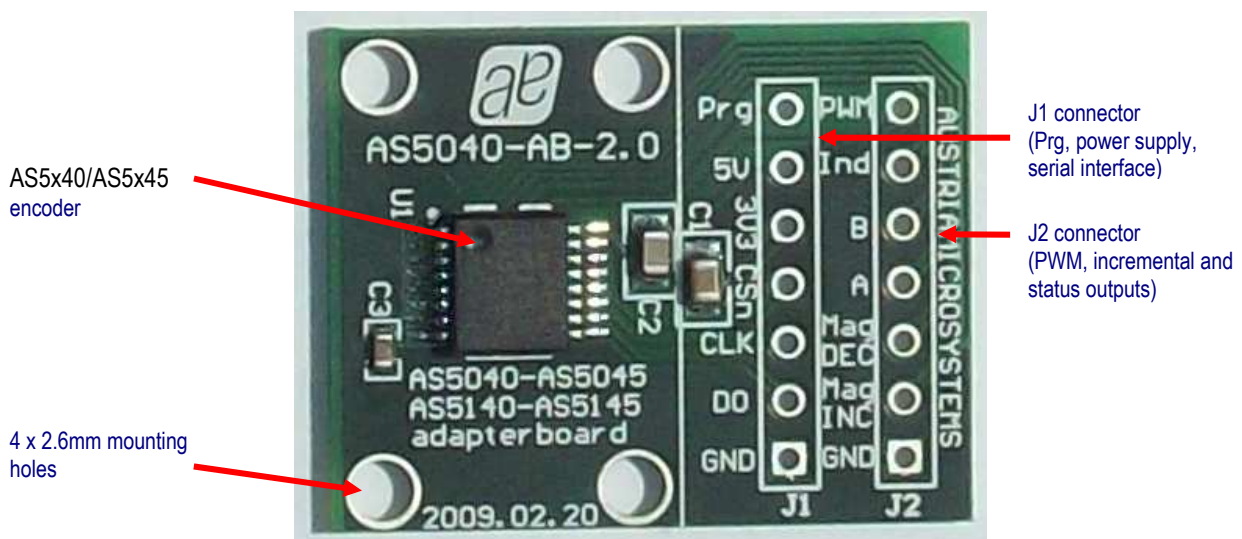


Figure 1: AS5040 Adapterboard

The technical drawing illustrates the AS5040 assembly in two views: a cross-section and a top view.

Cross-section view (top): This view shows the internal components of the assembly. A central rotating shaft, labeled "Rotating shaft and magnet holder", is supported by bearings. A magnet is mounted on the shaft, with a gap of 0.5~2mm between it and the AS5040 PCB. The PCB is mounted on a green hatched base, labeled "AS5040-AB PCB". The entire assembly is housed within a casing, secured by M2-M2.5 screws and nuts. A spacer is used to maintain the distance between the PCB and the base. The magnet is labeled "Magnet" and the PCB is labeled "AS5x4x".

Top view (bottom): This view shows the layout of the PCB. The overall dimensions are 22mm by 28mm. The PCB features a central circular area labeled "Diametral Magnet" with a diameter of 4x2.6mm. The magnet is divided into two halves, labeled "N" (North) and "S" (South). The PCB has two rows of pins: the top row includes PWM, Ind, B, A, Mag DEC, Mag INC, and GND; the bottom row includes Prg, 5V, 3V3, CSn, CLK, DO, and GND. The distance from the center of the magnet to the bottom edge of the PCB is 11mm, and the distance from the center to the side edges is 18mm.

A diametric magnet must be placed over on under the AS5x40/AS5x45 encoder, and should be centered on the middle of the package with a tolerance of 0.5mm.

The airgap between the magnet and the encoder casing should be maintained in the range 0.5mm~2mm.

The magnet holder must not be ferromagnetic. Materials as brass, copper, aluminum, stainless steel are the best choices to make this part.

3 AS5x40/AS5x45 and adapter board pinout

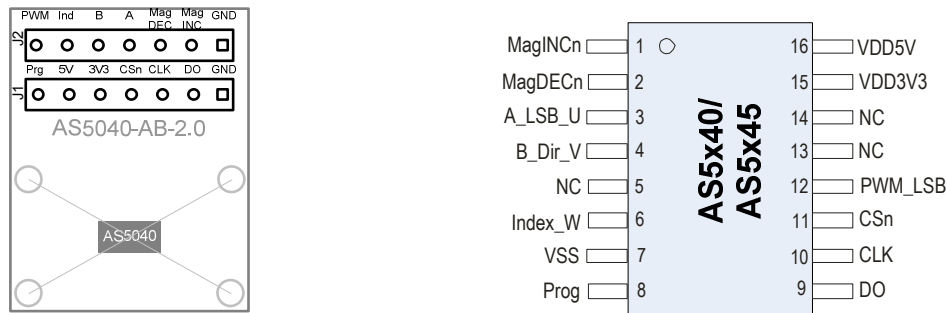


Figure 3: AS5040 adapter board connectors and encoder pinout

Pin# Board	Pin# AS5040	Symbol Board	Type	Description
JP1 - 1	7	GND	S	Supply ground
JP1 - 2	9	DO	DO_T	Data Output of Synchronous Serial Interface
JP1 - 3	10	CLK	DI_ST	Clock Input of Synchronous Serial Interface; Schmitt-Trigger input
JP1 - 4	11	CSn	DI_PU_ST	Chip Select, active low; Schmitt-Trigger input, internal pull-up resistor (~50kΩ) connect to VSS in incremental mode
JP1 - 5	15	3V3	S	3V-Regulator Output
JP1 - 6	16	5V	S	5V Supply
JP1 - 7	8	Prg	DI_PD	OTP P rogramming Input and Data Input for Daisy Chain mode. Internal pull-down resistor (~74kΩ). May be connected to VSS if programming is not used
JP2 - 1	7	GND	S	Supply ground
JP2 - 2	1	MagINC	DO_OD	Magnet Field M agnitude I NCrease; active low, indicates a distance reduction between the magnet and the device surface.
JP2 - 3	2	MagDEC	DO_OD	Magnet Field M agnitude D ECrease; active low, indicates a distance increase between the device and the magnet
JP2 - 4	3	A	DO	AS5040 – AS5140 – AS5145 (programmed) only Mode1.x: Quadrature A channel Mode2.x: Least Significant B it Mode3.x: U signal (phase1)
JP2 - 5	4	B	DO	AS5040 – AS5140 – AS5145 (programmed) only Mode1.x: Quadrature B channel quarter period shift to channel A. Mode2.x: D irection of Rotation Mode3.x: V signal (phase2)
JP2 - 6	6	Ind	DO	AS5040 – AS5140 – AS5145 (programmed) only Mode1.x and Mode2.x: I ndex signal indicates the absolute zero position Mode3.x: W signal (phase3) AS5045 – AS5145 (unprogrammed) Mode input, connect to VDD5 (Fast mode) or GND (Slow mode); do not change during operation
JP2 - 7	12	PWM	DO	Pulse W idth M odulation of approx. 1kHz; L SB in Mode3.x

Table 1: Pin description

Pin types:	DO_OD	digital output open drain	S	supply pin
	DO	digital output	DI	digital input
	DI_PD	digital input pull-down	DO_T	digital output / tri-state
	DI_PU	digital input pull-up	ST	Schmitt-Trigger input

4 Operation use cases

4.1 Standalone SSI output mode

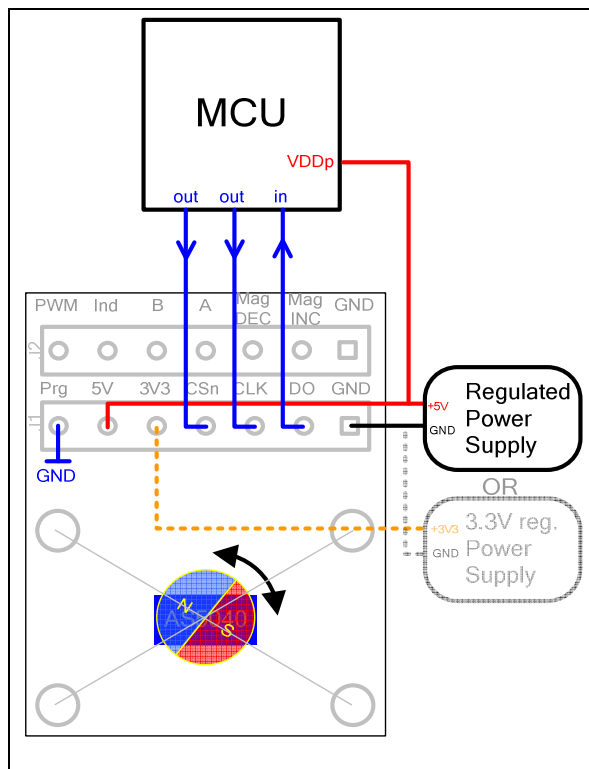


Figure 4: Using the analog output with the adapter board

The most complete and accurate solution for a MCU to read the angle of a magnet is the serial interface. The serial word contains 16 bits for AS5x40 or 18 bits for AS5x45 and consists of 10 bit (AS5x40) or 12 bit (AS5x45) angle value and some other indicator bits like MagINC, MagDEC, which can be read at the same time.

4.2 Standalone PWM output mode

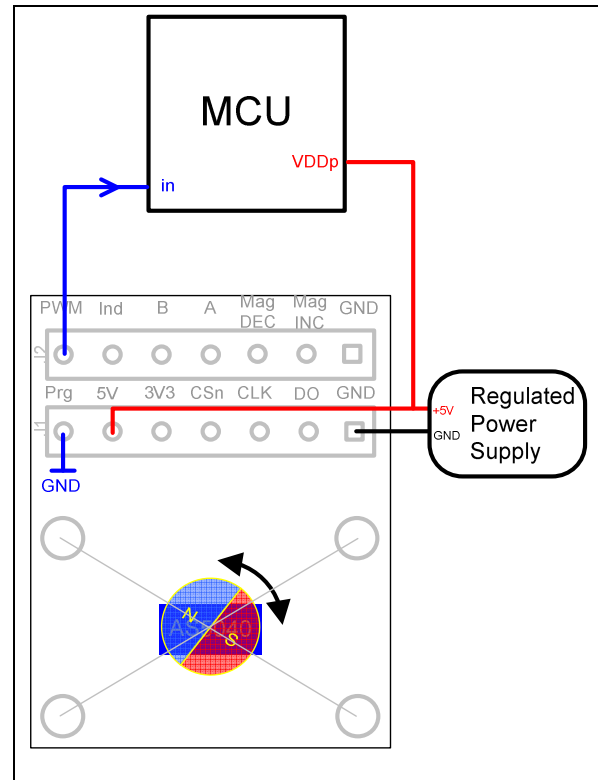


Figure 5: Using the PWM output with the adapter board

The AS5040 provides a pulse width modulated output (PWM), whose duty cycle is proportional to the measured angle. The PWM signal (J2 pin #7) with a period of 1025µs (1µs step) and 5V pulse voltage can be connected to the capture/timer input of a microcontroller in order to decode the angle value.

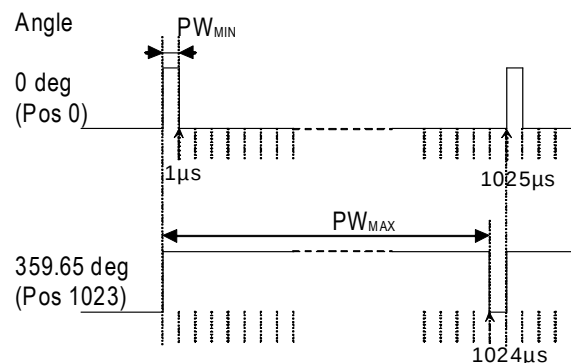


Figure 6: PWM duty cycle depending on magnet position

4.3 Standalone incremental output

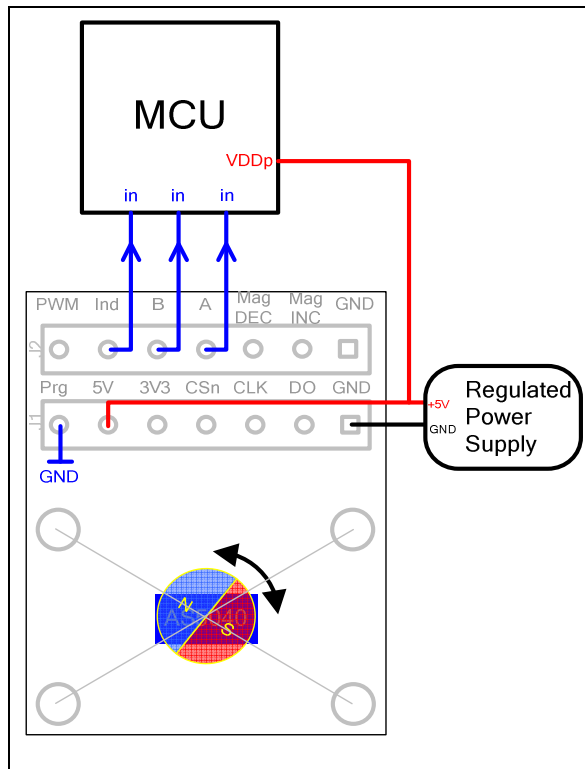


Figure 7: Using the Incremental output with the adapter board

Three different incremental output modes are possible with quadrature A/B being the default mode (two-channel quadrature, step / direction incremental signal (LSB) and the direction bit in clockwise (CW) and counter-clockwise (CCW) direction. The pre-programmed version AS5145-I10 provides a 10bit and AS5145-I12 a 12bit incremental output.

The AS5045 do not offer an incremental output (see chapter 6).

For further information, please refer to datasheet.

The diagram illustrates a multi-board system architecture. At the top, an MCU (Microcontroller Unit) is connected to a Regulated Power Supply. The MCU has three pins labeled 'out', 'out', and 'in'. The 'in' pin is connected to the +5V output of the Regulated Power Supply, which is also labeled 'VDDp'. The 'out' pins are connected to the 'Prg' and '5V' pins of the top board's connector. The Regulated Power Supply is connected to the '+5V' and 'GND' pins of the top board's connector. The top board is connected to a motor driver (represented by a circular symbol with 'A' and 'B' terminals) via a 100R resistor and a 1nF capacitor. The bottom board is connected to the motor driver via a 100R resistor and a 1nF capacitor. The bottom board is connected to the 'next Board' via a 100R resistor and a 1nF capacitor. The bottom board is also connected to the 'next Board' via a 100R resistor and a 1nF capacitor.

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5 Programming the AS5040

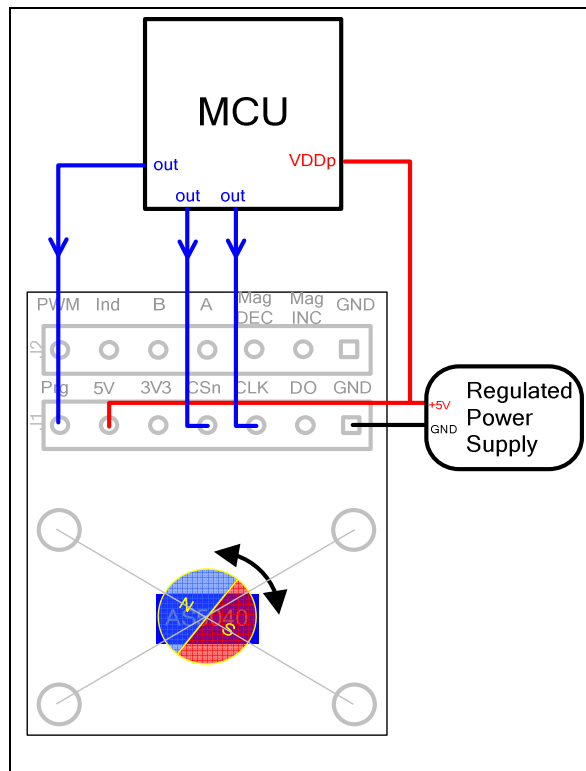


Figure 9: Programming the AS5040

After power-on, programming the AS5040 is enabled with the rising edge of CSn with Prog = high and CLK = low. 16 bit configuration data must be serially shifted into the OTP register via the Prog-pin. The first "CCW" bit is followed by the zero position data (MSB first / 10 bit for AS5x40 and 12 bit for AS5x45) and some mode settings (please refer to datasheet). Data must be valid at the rising edge of CLK.

After writing data into the OTP register it can be permanently programmed by rising the Prog pin to the programming voltage V_{PROG} of 7.3-7.5V. 16 CLK pulses must be applied to program the fuses. For exiting the programming mode, the chip must be reset by a power-on-reset. The programmed data is available after the next power-up.

6 AS5040 Differences to AS5045

Building Block	AS5045	AS5040
Resolution	12bits, 0.088°/step.	10bit, 0.35°/step
Data length	read: 18bits (12bits data + 6 bits status) OTP write: 18 bits (12bits zero position + 6 bits mode selection)	read: 16bits (10bits data + 6 bits status) OTP write: 16 bits (10bits zero position + 6 bits mode selection)
incremental encoder	Not used Pin 3: not used Pin 4: not used	quadrature, step/direction and BLDC motor commutation modes Pin 3: incremental output A_LSB_U Pin 4: incremental output B_DIR_V
Pins 1 and 2	MagINCn, MagDECn: same feature as AS5040, additional OTP option for red-yellow-green magnetic range	MagINCn, MagDECn indicate in-range or out-of-range magnetic field plus movement of magnet in z-axis
Pin 6	MODE pin, switch between fast and slow mode	Pin 6: Index output
Pin 12	PWM output: frequency selectable by OTP: 1µs / step, 4096 steps per revolution, f=244Hz 2µs / step, 4096 steps per revolution, f=122Hz	PWM output: 1µs / step, 1024 steps per revolution, 976Hz PWM frequency
sampling frequency	selectable by MODE input pin: 2.5kHz, 10kHz	fixed at 10kHz @10bit resolution
Propagation delay	384µs (slow mode) 96µs (fast mode)	48µs
Transition noise (rms; 1sigma)	0.03 degrees max. (slow mode) 0.06 degrees max. (fast mode)	0.12 degrees
OTP programming options	zero position, rotational direction, PWM disable, 2 Magnetic Field indicator modes, 2 PWM frequencies	zero position, rotational direction, incremental modes, index bit width

7 AS5040 adapter board hardware

7.1 AS5040-AB-2.0 schematics

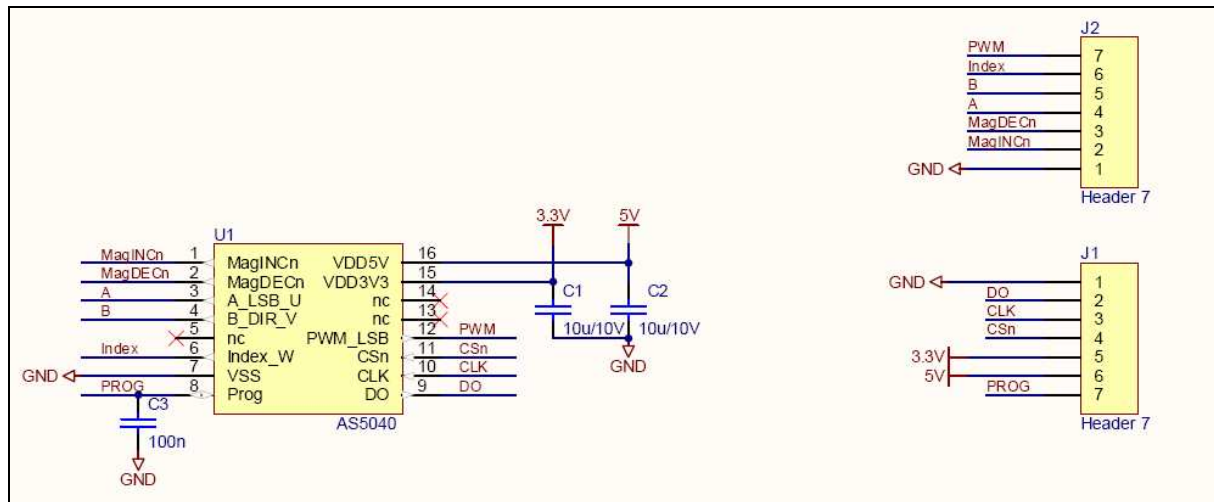


Figure 10: AS5040-AB-2.0 adapterboard schematics

7.2 AS5040-AB-2.0 PCB layout

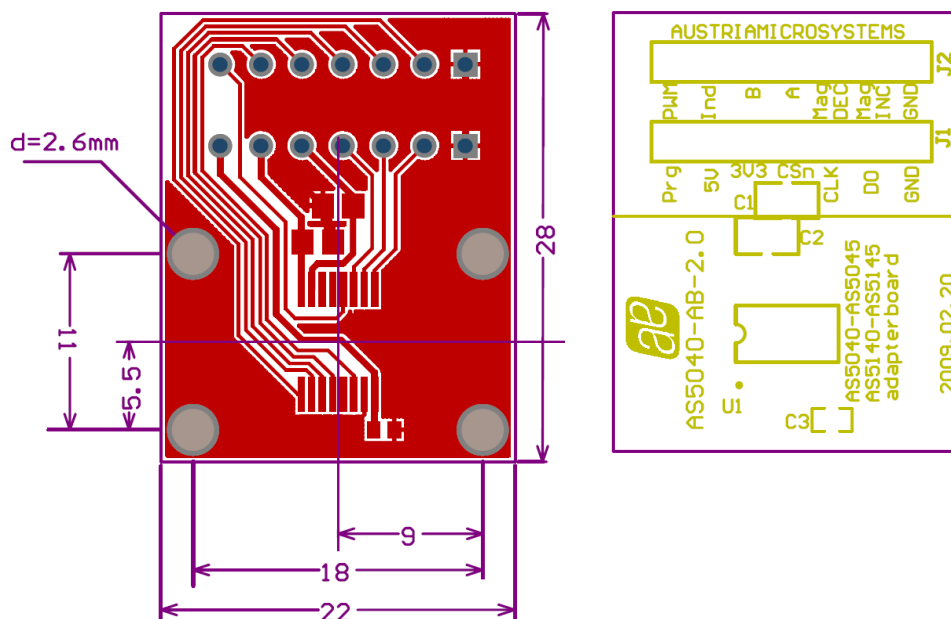


Figure 11: AS5040-AB-2.0 adapter board layout

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